

PATENT NUMBER

U.S. REISSUE Patent Application

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SCANNED <u>104</u> Q.A. <u>AG</u>	<u>7/16/2002</u>	

APPLICATION NO. 10/032431	CONT/PRIOR D F	CLASS 365	SUBCLASS 203	ART UNIT 2818	EXAMINER T. Dinh
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APPLICANTS

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Title

Dynamic random access memory using imperfect isolating transistors

PTO-2040
12/88

The original patent was surrendered in parent reissue 08/853,507.

☒ SURRENDER OF ORIGINAL PATENT TED (Exr. Initials) ORIGINAL PATENT NUMBER 5414,662

ISSUING CLASSIFICATION

ORIGINAL			CROSS REFERENCE(S)						
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)						
365	203	365	149	208					
INTERNATIONAL CLASSIFICATION									
G11C	702								

☐ Continued on Issue Slip Inside File Jacket

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg. <u>3</u>	Figs. Drwg. <u>3</u>	Print Fig. <u>2</u>	Total Claims <u>25</u>	Print Claim for O.G. <u>18</u>
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) _____ (Date)			NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____	<u>can</u> TAN DINH PRIMARY EXAMINER _____ (Primary Examiner) _____ (Date) <u>8/16/03</u>			ISSUE FEE	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) _____ (Date)			Amount Due	Date Paid
FINAL SPRE REVIEW _____ (INITIALS)	ISSUE BATCH NUMBER				
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